A recruitment procedure is open to hire early-stage (MSc) researchers in the framework of the project SPIRS:

Requirements (qualifications and skills)

- Master's degree in the field of microelectronics design, electronics systems, electronics and computer engineering, or similar;

- English language at a level that allows editing scientific texts and giving presentations at international scientific conferences;

- Ability to think analytically and learn quickly to acquire new skills;

- Strong self-motivation for scientific work;

- An additional advantage will be previous experience in Digital Design and FPGA prototyping, and solid background in HDL languages (VHDL/Verilog).

Offer description

- Full-time contract for initial 12 months with the possibility of extension up to 36 months, with the aim of obtaining a PhD degree. Salary will depend on experience, according to CSIC's salary scale.