

**ABOUT IMSE**

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RESOURCES

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JOB & TRAINING

At IMSE
At CSIC

Power Efficiency & Consumption Measurements

The need to reduce energy consumption as well as CO2 emissions is driving the growth of power electronics and power converters. Two of the main power electronics design drivers are increased power conversion efficiency and consumption analysis.

Full Day Agenda:

09:00 Welcome & Overview.
09:15 Power Device Characterization.
10:15 Software for Power Device Design & Simulation.
10:45 Coffee break.
11:15 Power Device Design & Debugging.
12:30 Power Device Validation & Certification.
13:30 Closing.

Places are limited.

Registration is free at:

contactcenter_spain@keysight.com || [web](#) || Phone: 800 000 154

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