

Webinar XFAB

On-Chip ESD Protection in Complex Analog/Mixed-Signal and High-Voltage Designs

Electrostatic discharge (ESD) is a serious threat to integrated circuits (ICs) that can cause irreversible damage. This webinar on ESD protection will show you solutions on how to eliminate ESD threats in complex analog/mixed-signal and high-voltage designs. It covers an overview of various ESD protection concepts, and explains the structures and schemes available to protect against electrostatic discharge in X-FAB's enhanced 0.35 and 0.18 micrometer XH035 and XH018 high-voltage foundry processes. The webinar presentation also highlights similarities and differences among ESD protection concepts, outlining the advantages and disadvantages of each in circuit designs.

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